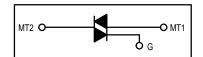
### **Triacs**

### **Silicon Bidirectional Thyristors**

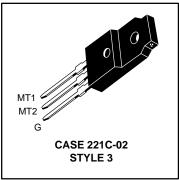
... designed primarily for full-wave ac control applications, such as light dimmers, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Four Modes



## MAC210AFP Series

ISOLATED TRIACS THYRISTORS 10 AMPERES RMS 600 thru 800 VOLTS



#### **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
Repetitive Peak Off-State Voltage <sup>(1)</sup> (T <sub>J</sub> = -40 to +125°C) 1/2 Sine Wave 50 to 60 Hz, Gate Open	V <sub>DRM</sub>		Volts
MAC210A8FP MAC210A10FP		600 800	
On-State RMS Current (T <sub>C</sub> = +70°C) Full Cycle Sine Wave 50 to 60 Hz(2)	I <sub>T</sub> (RMS)	10	Amps
Peak Nonrepetitive Surge Current (One Full Cycle, 60 Hz, T <sub>C</sub> = +70°C) preceded and followed by rated current	ITSM	100	Amps
Circuit Fusing (t = 8.3 ms)	I <sup>2</sup> t	40	A <sup>2</sup> s
Peak Gate Power (T <sub>C</sub> = +70°C, Pulse Width = 10 μs)	P <sub>GM</sub>	20	Watts
Average Gate Power ( $T_C = +70^{\circ}C$ , $t = 8.3 \text{ ms}$ )	PG(AV)	0.35	Watt
Peak Gate Current (T <sub>C</sub> = +70°C, Pulse Width = 10 μs)	IGM	2	Amps
RMS Isolation Voltage ( $T_A = 25^{\circ}C$ , Relative Humidity $\leq 20\%$ )	V <sub>(ISO)</sub>	1500	Volts
Operating Junction Temperature	TJ	-40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +125	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	2.2	°C/W
Thermal Resistance, Case to Sink	$R_{\theta CS}$	2.2 (typ)	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	60	°C/W

- 1. V<sub>DRM</sub> for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.
- 2. The case temperature reference point for all T<sub>C</sub> measurements is a point on the center lead of the package as close as possible to the plastic body.



### **MAC210AFP Series**

### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current (Either Direction) $ (V_D = \text{Rated V}_{DRM}, \text{ Gate Open})  T_J = 25^{\circ}\text{C} $ $ T_J = +125^{\circ}\text{C} $	IDRM	_		10 2	μA mA
Peak On-State Voltage (Either Direction) (I <sub>TM</sub> = 14 A Peak; Pulse Width = 1 to 2 ms, Duty Cycle ≤ 2%)	VTM	_	1.2	1.65	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, $R_L$ = 100 Ohms Minimum Gate Pulse Width = 2 $\mu$ s) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+)	lGT	_ _ _ _	12 12 20 35	50 50 50 75	mA
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, $R_L$ = 100 Ohms Minimum Gate Pulse Width = 2 $\mu$ s) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+) (Main Terminal Voltage = Rated VDRM, $R_L$ = 10 k $\Omega$ , $T_J$ = +125°C) MT2(+), G(+); MT2(+), G(-); MT2(-), G(-)	VGT		0.9 0.9 1.1 1.4	2 2 2 2.5 —	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 500 mA, T <sub>C</sub> = +25°C)	lн	_	6	50	mA
Turn-On Time (Rated $V_{DRM}$ , $I_{TM}$ = 14 A, $I_{GT}$ = 120 mA, Rise Time = 0.1 $\mu$ s, Pulse Width = 2 $\mu$ s)	<sup>t</sup> gt		1.5	_	μs
Critical Rate of Rise of Commutation Voltage ( $V_D$ = Rated $V_{DRM}$ , $I_{TM}$ = 14 A, Commutating di/dt = 5.0 A/ms, Gate Unenergized, $T_C$ = +70°C)	dv/dt <sub>(c)</sub>		5	_	V/µs
Critical Rate of Rise of Off–State Voltage ( $V_D$ = Rated $V_{DRM}$ , Exponential Voltage Rise, Gate Open, $T_C$ = +70°C)	dv/dt		100	_	V/µs

### **TYPICAL CHARACTERISTICS**

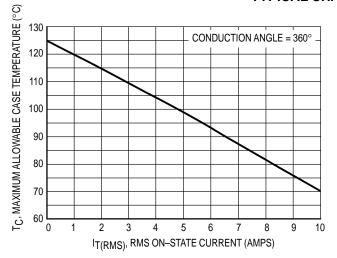


Figure 1. Current Derating

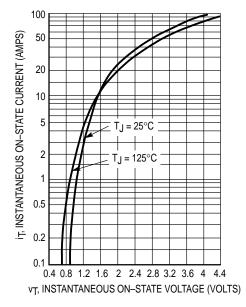


Figure 3. Maximum On-State Characteristics

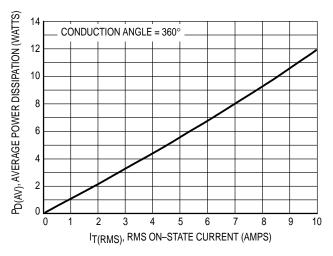


Figure 2. Power Dissipation

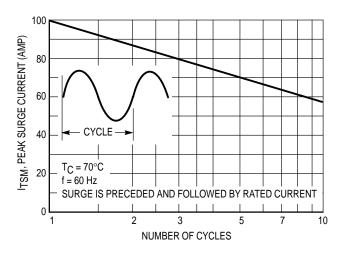


Figure 4. Maximum Nonrepetitive Surge Current

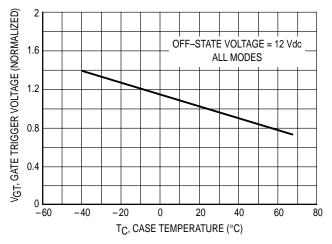
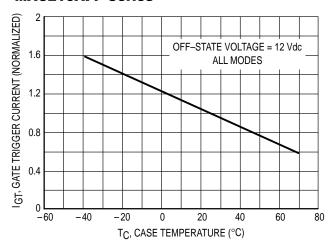


Figure 5. Typical Gate Trigger Voltage

### **MAC210AFP Series**



**Figure 6. Typical Gate Trigger Current** 

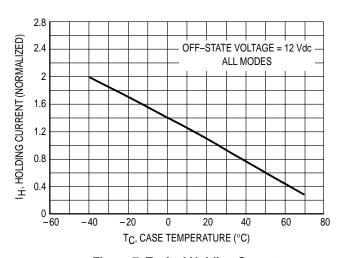


Figure 7. Typical Holding Current

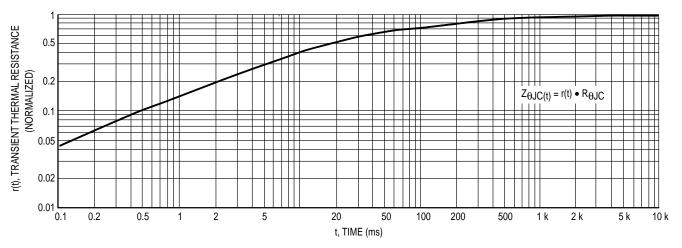
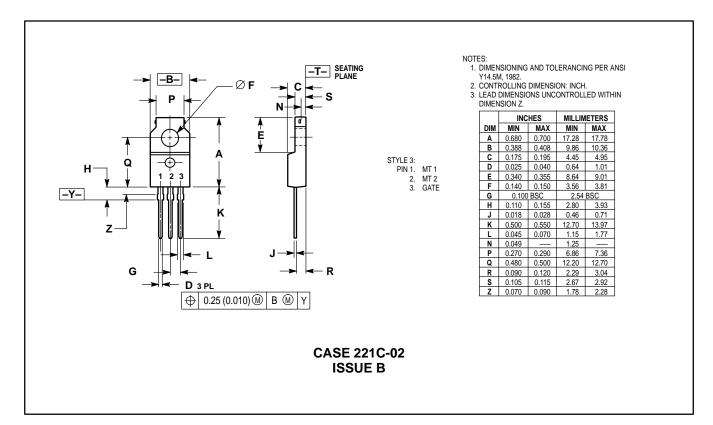


Figure 8. Thermal Response

### **PACKAGE DIMENSIONS**



# **NOTES**

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